

Form PTO-1449 (modified)

List of Patents and Publications for Applicant's

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Atty. Docket No.

ALTR:023

Serial No.

10/757,928

Applicant

HUANG ET AL.

Filing Date:

1/15/04

Group:

~~2819~~ 2827

U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page

Other Art

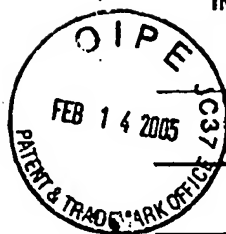
See Page 2

U.S. Patent Documents**U.S. Patent Documents**

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Filing Date if App. |
|-------------|-----------|----------------------|----------|-------------------|-------|-----------|---------------------|
| MW | A1 | 4,281,398 | 7/28/81 | McKenny et al. | | | 2/12/80 |
| MW | A2 | US2002/01058 54A1 | 8/8/02 | Huh et al. | | | 1/23/02 |
| MW | A3 | US2003/02064 77A1 | 11/6/03 | Waller et al. | | | 5/22/03 |
| MW | A4 | US2004/00221 10A1 | 2/5/04 | Haraguchi et al. | | | 2/25/03 |
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| MW | A6 | 4,724,422 | 2/9/88 | Golab | | | 9/13/85 |
| MW | A7 | 4,829,481 | 5/9/89 | Johnson et al. | | | 10/22/87 |
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| MW | A10 | 5,821,770 | 10/13/98 | Rees | | | 4/30/96 |
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Examiner: *Michael Rosenberg*Date Considered: *3/10/2006*

EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



(2/14/05 cont'd)

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|---|--------------------------------------|--------------------------------|
| Form PTO-1449 (modified) List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary) | Atty. Docket No. ALTR:023 | Serial No. 10/757,928 |
| Applicant HUANG ET AL. | | |
| Filing Date: 1/15/04 | | Group: 2819 2827 |
| U.S. Patent Documents See Page 1 | Foreign Patent Documents See Page | Other Art See Page 2 |

Foreign Patent Documents

| Foreign Patent Documents | | | | | | | |
|--------------------------|-----------|-----------------|------|---------|-------|-----------|--------------------|
| Exam. Init. | Ref. Des. | Document Number | Date | Country | Class | Sub Class | Translation Yes/No |
| | B1 | | | | | | |

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

| Other Art (Including Author, Title, Date, Pertinent Pages, Etc.) | | |
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| Exam. Init. | Ref. Des. | Citation |
| W | C1 | Ouellette et al., "Shared Fuse Macro For Multiple Embedded Memory Devices With Redundancy", IEEE Custom Integrated Circuits Conference, 2001, pps. 191-194. |
| W | C2 | McCollum et al., "Reliability Of Antifuse-Based Field Programmable Gate Arrays For Military And Aerospace Applications", Actel Corporation, Abstract, September 2001, 6 pgs. |
| W | C3 | Benedetto et al., "Amorphous Silicon Antifuse Programmable-Array-Logic Devices For High Reliability Space Applications", UTMIC Microelectronic Systems, January 2002, 4 pgs. |
| W | C4 | Laville et al., "Integrated Offset Trimming Technique", 27 th European Solid-State Circuits Conference, September 2001, 4 pgs. |
| W | C5 | Actel, Reliability Considerations For Automotive FPGAs, White Paper, September 2003, 17 pgs. |
| W | C6 | MRAM Redundancy Scheme (prior art available prior to filing date of the present application) |

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|---|-----------------------------------|
| Examiner: <u>Michael Weimberg</u> | Date Considered: <u>3/10/2006</u> |
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